

# 2SK1157, 2SK1158

# Silicon N Channel MOS FET

REJ03G0910-0200

(Previous: ADE-208-1248)

Rev.2.00 Sep 07, 2005

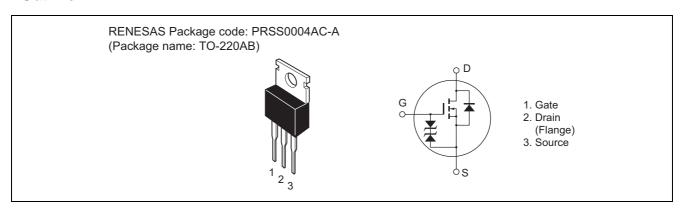
### **Application**

High speed power switching

#### **Features**

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter and motor driver

#### **Outline**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

| ltem                                      |         | Symbol                   | Ratings     | Unit |  |
|---|---------|--------------------------|-------------|------|--|
| Drain to source voltage                   | 2SK1157 | $V_{DSS}$                | 450         | V    |  |
|   | 2SK1158 |                          | 500         |      |  |
| Gate to source voltage                    |         | $V_{GSS}$                | ±30         | V    |  |
| Drain current                             |         | I <sub>D</sub>           | 7           | А    |  |
| Drain peak current                        |         | I <sub>D(pulse)</sub> *1 | 28          | Α    |  |
| Body to drain diode reverse drain current |         | I <sub>DR</sub>          | 7           | А    |  |
| Channel dissipation                       |         | Pch*2                    | 60          | W    |  |
| Channel temperature                       |         | Tch                      | 150         | °C   |  |
| Storage temperature                       |         | Tstg                     | −55 to +150 | °C   |  |

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at  $T_C = 25$ °C

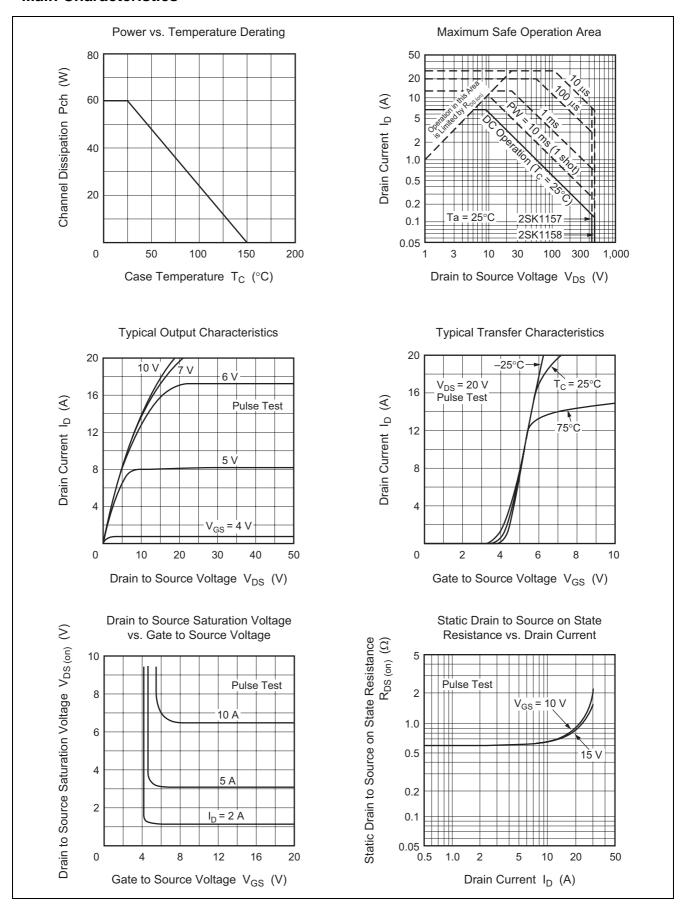
#### **Electrical Characteristics**

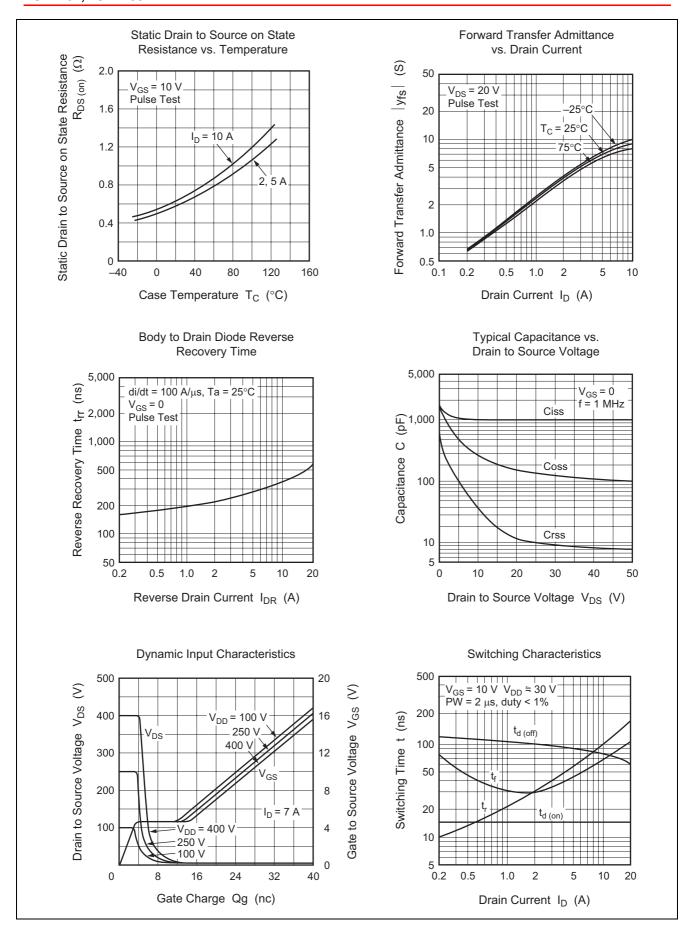
 $(Ta = 25^{\circ}C)$ 

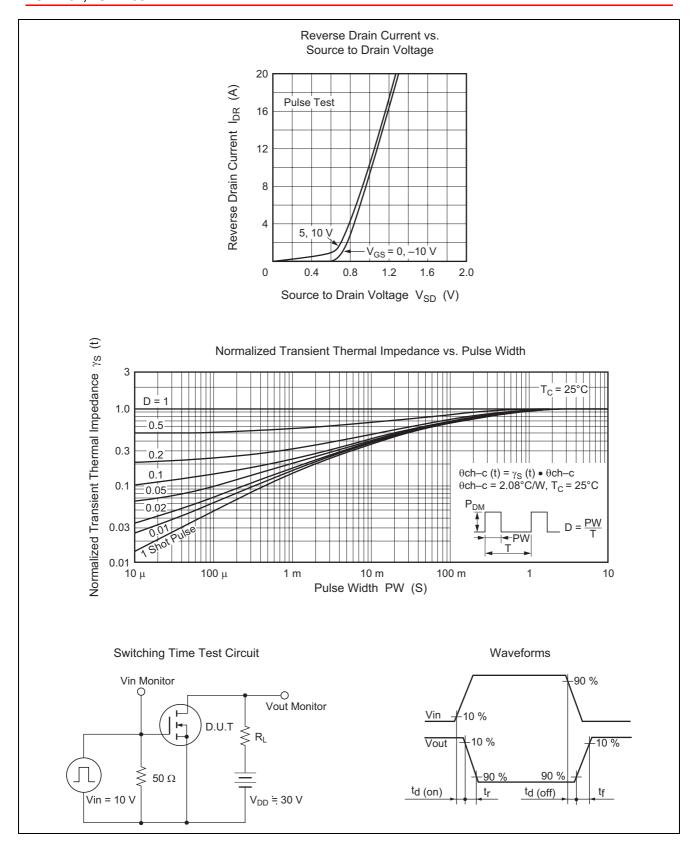
| Item                                 |         | Symbol               | Min | Тур  | Max | Unit | Test conditions                                 |
|--------------------------------------|---------|----------------------|-----|------|-----|------|---|
| Drain to source                      | 2SK1157 | $V_{(BR)DSS}$        | 450 | _    | _   | V    | $I_D = 10 \text{ mA}, V_{GS} = 0$               |
| breakdown voltage                    | 2SK1158 |                      | 500 |      |     |      |   |
| Gate to source breakdown voltage     |         | $V_{(BR)GSS}$        | ±30 | 1    | _   | V    | $I_G = \pm 100 \ \mu A, \ V_{DS} = 0$           |
| Gate to source leak current          |         | $I_{GSS}$            | _   | _    | ±10 | μΑ   | $V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$         |
| Zero gate voltage drain              | 2SK1157 | $I_{DSS}$            | _   | _    | 250 | μΑ   | $V_{DS} = 360 \text{ V}, V_{GS} = 0$            |
| current                              | 2SK1158 |                      |     |      |     |      | $V_{DS} = 400 \text{ V}, V_{GS} = 0$            |
| Gate to source cutoff voltage        |         | V <sub>GS(off)</sub> | 2.0 | _    | 3.0 | V    | $I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$     |
| Static drain to source on            | 2SK1157 | R <sub>DS(on)</sub>  | _   | 0.6  | 0.8 | Ω    | $I_D = 4 \text{ A}, V_{GS} = 10 \text{ V}^{*3}$ |
| state resistance                     | 2SK1158 |                      | _   | 0.7  | 0.9 |      |   |
| Forward transfer admittance          |         | y <sub>fs</sub>      | 4.0 | 6.5  | _   | S    | $I_D = 4 \text{ A}, V_{DS} = 10 \text{ V}^{*3}$ |
| Input capacitance                    |         | Ciss                 | _   | 1050 | _   | pF   | $V_{DS} = 10 \text{ V}, V_{GS} = 0,$            |
| Output capacitance                   |         | Coss                 | _   | 280  | _   | pF   | f = 1 MHz                                       |
| Reverse transfer capacitance         |         | Crss                 | _   | 40   | _   | pF   |   |
| Turn-on delay time                   |         | t <sub>d(on)</sub>   | _   | 15   | _   | ns   | $I_D = 4 A$ , $V_{GS} = 10 V$ ,                 |
| Rise time                            |         | t <sub>r</sub>       | _   | 55   | _   | ns   | $R_L = 7.5 \Omega$                              |
| Turn-off delay time                  |         | $t_{d(off)}$         | _   | 95   | _   | ns   |   |
| Fall time                            |         | t <sub>f</sub>       | _   | 40   | _   | ns   |   |
| Body to drain diode forward voltage  |         | $V_{DF}$             | _   | 0.95 | _   | V    | $I_F = 7 \text{ A}, V_{GS} = 0$                 |
| Body to drain diode reverse recovery |         | t <sub>rr</sub>      | _   | 320  | _   | ns   | $I_F = 7 \text{ A}, V_{GS} = 0,$                |
| time                                 |         |                      |     |      |     |      | $di_F/dt = 100 A/\mu s$                         |

Note: 3. Pulse test

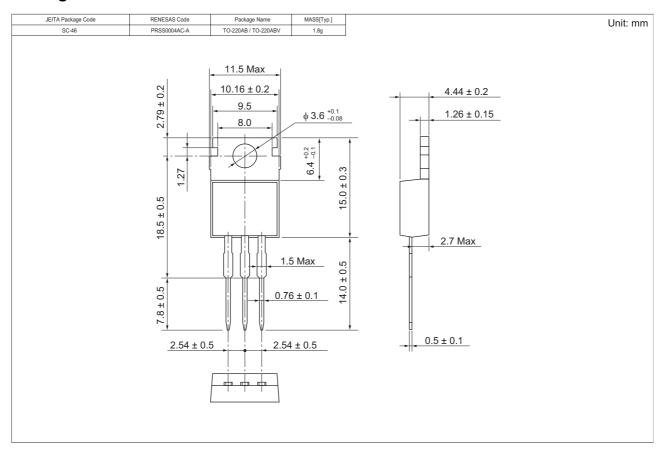
#### **Main Characteristics**







# **Package Dimensions**



# **Ordering Information**

| Part Name | Quantity | Shipping Container |
|-----------|----------|--------------------|
| 2SK1157-E | 500 pcs  | Box (Sack)         |
| 2SK1158-E | 500 pcs  | Box (Sack)         |

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